

PATENT APPLICATION

Atty. Docket No.: 8750-019 Serial No. 10/002,542
Applicant: Chang-Yong LEE, et al.
Filing Date: November 13, 2001 Group: 2818

**INFORMATION DISCLOSURE CITATION
FORM PTO-1449 (Modified)****U.S. PATENT DOCUMENTS**

<u>Exam</u> <u>Init</u>	<u>Ref</u>	<u>Document</u> <u>Number</u>	<u>Issue</u> <u>Date</u>	<u>Name</u>	<u>Class</u>	<u>Sub-</u> <u>Class</u>
<u>TL</u>		6,084,803	Jul 4, 2000	Sredanovic et al		

FOREIGN PATENT DOCUMENTS

<u>Document</u> <u>Number</u>	<u>Publication</u> <u>Date</u>	<u>Country</u>	<u>Name</u>
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<u>Exam</u> <u>Init</u>	<u>Ref</u>	<u>(Including Author, Title, Date, Pertinent Pages, etc.)</u>
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Examiner: Thong Le

Date Considered: 7/18/02

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U.S. PATENT DOCUMENTS

<u>Exam Init</u>	<u>Ref</u>	<u>Document Number</u>	<u>Issue Date</u>	<u>Name</u>	<u>Class</u>	<u>Sub Class</u>
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OTHER DOCUMENTS

<u>Exam Init</u>	<u>Ref</u>	<u>Author, Title, Date, Pertinent Pages, Etc.)</u>
TL	—	Sato, et al. Fast Cycle Ram(FCRAM); a 20-ns Random Row Access, Pipe-Lined Operating Dram 1998 page 22-25
TL	—	Taguchi, et al. A 40-ns 64-Mb Dram with 64-b Parallel Data Bus Architecture November 1991 page 1493-1497

Examiner: ThierryDate Considered: 7/18/02